

11

12

13

14

15

16

17

18

19

interleaved:

and constructions shown and described, since various other modifications may occur to those with ordinary skill in the art.

## IN THE CLAIMS:

10. (Twice Amended) DC to DC switching circuit for
2 controlling power switching devices in a DC to DC converter
3 having first and second converter circuits operating into a
4 common load comprising:

a first pulse width modulator controlling the power switching devices of the first converter circuit;

a second pulse width modulator controlling the power switching devices of the second converter circuit;

a feedback circuit responsive to the voltage across the common load;

control circuits for controlling the first and second pulse width modulators responsive to the feedback circuit, the operation of the first and second pulse width modulators being

the control circuits also being responsive to the difference in <u>currents</u> [current] through the first converter and the second converter to adjust the relative duty cycle of the first and second converters to tend to minimize the difference in the voltage across a sense resistor;

- the first pulse width modulator, the second pulse width 20 modulator, the feedback circuit and the control circuits being in a single integrated circuit.
  - (Three Times Amended) A DC to DC converter having a 22. plurality of converter circuits for operating into a common load, comprising:
  - a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;
- a plurality of pulse width modulators driven by a common 9 oscillator in an interleaved manner, each pulse width modulator 10 controlling one of the plurality of buck converter circuits, 11 whereby the operation of the buck converter circuits is 12
- a feedback circult responsive to a voltage across the common 14
- load [output]; 15
- a voltage control circuit controlling the plurality of pulse 16 17 width modulators responsive to the feedback circuit and a commanded output voltage; and
- a current balance control circuit responsive to the 19 difference in currents [current] in the plurality of interleaved 20

buck converter circuits and controlling the pulse width

interleaved:

1

13

18

modulators to balance the <u>currents</u> [current] in the plurality of interleaved buck converter circuits;

the plurality of pulse width modulators and the control circuits being in a single integrated circuit.

500

32. (Three Times Amended) A DC to DC converter having a plurality of converter circuits operating into a common load, comprising:

a plurality of buck converter circuits operating into the

common load, each buck converter circuit having an inductor for

alternately conducting between the first [and second] power

supply terminal and the common load, [terminals,] and the second

power supply terminal and the common load;

a plurality of pulse width modulators each controlling one of the plurality of buck converter circuits, the operation of the pulse width modulators and the buck converter circuits being interleaved;

a feedback circuit responsive to a voltage across the common load:

control circuits responsive to the feedback circuit and a commanded output voltage to control a nominal duty cycle of the plurality of buck converter circuits, the control circuits also being responsive to the difference in <u>currents</u> [current] in the plurality of interleaved buck converter circuits to adjust [a] relative duty cycles [cycle] of the plurality of buck converter

9

10

11

12

15

18

19

circuits to balance the <u>currents</u> [current] in the buck converter circuits;

the plurality of pulse width modulators and the control circuits being in a single integrated circuit.

45. (Three Times Amended) A DC to DC converter having a plurality of converter circuits operating into a common load, comprising:

a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;

a plurality of pulse width modulators each controlling one
of the plurality of buck converter circuits, the operation of the
pulse width modulators being interleaved;

control circuits for adjusting a nominal duty cycle of the plurality of interleaved buck converter circuits, the control circuits also being responsive to the difference in <u>currents</u> [current] in the plurality of interleaved buck converter circuits to adjust the relative duty <u>cycles</u> [cycle] of the plurality of buck converter circuits to balance the <u>currents</u> [current] therein;

the plurality of pulse width modulators and the control circuits being in a single integrated circuit.

24

1

2

12

13

14

15

16

17

- 1 46. (Twice Amended) A DC to DC converter having first and
- 2 second converter circuits operating into a common load,
- 3 comprising:
- first and second buck converter circuits operating into the common load, each buck converter circuit having an inductor for
- $\$  alternately conducting between  $\underline{\text{the}}$  first [and second] power
  - supply terminal and the common load, [terminals,] and the second
- 8 power supply terminal and the common load;
- 9 a first pulse width modulator controlling the first buck 10 converter circuit;
- a second pulse width modulator controlling the second buck converter circuit;
  - a feedback circuit responsive to the voltage across the common load;
  - control circuits for controlling the first and second pulse
  - 16 width modulators responsive to the feedback circuit;
  - the control circuits also being responsive to current
  - 18 measurements in the first buck converter circuit and the second
  - 19 buck converter circuit for adjusting the relative duty cycle of
  - 20 the first and second pulse width modulators to balance the
  - 21 currents in the buck converter circuits;
  - the first pulse width modulator, the second pulse width
  - 23 modulator, the feedback circuit and the control circuits being in
  - 24 a single integrated circuit.

- 1 47. (Twice Amended) A DC to DC converter having a 2 plurality of converter circuits operating into a common load,
- 3 comprising:
- a plurality of buck converter circuits operating into the
- 5 common load, each buck converter circuit having an inductor for
- 6 alternately conducting between the first [and second] power
  - supply terminal and the common load, [terminals,] and the second
  - power supply terminal and the common load;
- a plurality of pulse width modulators driven by a common
- 10 oscillator in an interleaved manner, each pulse width modulator
- 11 controlling one of the plurality of buck converter circuits,
- 12 whereby the operation of the buck converter circuits is
- 13 interleaved;
- a feedback circuit responsive to a voltage across the common
- 15 load:
- a voltage control circuit for controlling the plurality of
- 17 pulse width modulators responsive to the feedback circuit and a
- 18 commanded output voltage; and
- a current balance control circuit responsive to the
- 20 difference in currents [current] in the plurality of interleaved
- 21 buck converter circuits for controlling the pulse width
- 22 modulators to balance the currents [current] in the plurality of
- 23 interleaved buck converter circuits;

- the plurality of pulse width modulators, the feedback 24 circuit, the voltage control circuit and the current balance 25 control circuit being in a single integrated circuit. 26 (Twice Amended) A DC to DC converter having a 48. 1 plurality of converter circuits operating into a common load, 2 3 comprising: a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power
  - a plurality of pulse width modulators each controlling power switching devices of one of the plurality of interleaved buck converter circuits, the operation of the pulse width modulators and the buck converter circuits being interleaved;

supply terminal and the common load, [terminals,] and the second

power supply terminal and the common load;

- a feedback circuit responsive to a voltage across the common 13 load; 14
- control circuits responsive to the feedback circuit and a commanded output voltage to control a nominal duty cycle of the plurality of buck converter circuits, the control circuits also being responsive to the difference in currents [current] in the plurality of interleaved buck converter circuits to adjust the relative duty cycles [cycle] of the plurality of buck converter 20

7

8

9

10

16

17

18

- 21 circuits to balance the <u>currents</u> [current] in the buck converter
- 22 circuits;
- the plurality of pulse width modulators, the feedback
- 24 circuit and the control circuits being in a single integrated
- 25 circuit.
  - 1 49. (Twice Amended) A DC to DC converter having a
  - 2 plurality of converter circuits operating into a common load,
    - comprising:
      - a plurality of buck converter circuits operating into the
  - 5 common load, each buck converter circuit having an inductor for
  - 6 alternately conducting between the first [and second] power
  - 7 supply terminal and the common load, [terminals,] and the second
  - 8 power supply terminal and the common load;
  - a plurality of pulse width modulators each controlling one
- 10 of the plurality of buck converter circuits, the pulse width
- 11 modulators being driven by a common oscillator signal so that the
- 12 operation of the pulse width modulators is interleaved;
- control circuits for adjusting a nominal duty cycle of the
- 14 plurality of interleaved buck converter circuits to control a
- 15 voltage on the common load, and for responding to the difference
- 16 in currents [current] in the plurality of interleaved buck
- 17 converter circuits to adjust the relative duty cycles [cycle] of
- 18 the plurality of buck converter circuits to balance the currents
- 19 [current] in the buck conventer circuits;

- 20 <u>the plurality of pulse width modulators and the control</u>
- 21 circuits being in a single integrated circuit.
  - 1 50. (Twice Amended) A DC to DC converter having first and
  - 2 second converter circuits operating into a common load,
  - 3 comprising:

first and second buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second

- 8 power supply terminal and the common load;
- 9 a first pulse width modulator controlling the first buck 10 converter circuit;
- a second pulse width modulator controlling the second buck converter circuit;
- a feedback circuit responsive to the voltage across the common load;
- 15 control circuits for controlling the first and second pulse 16 width modulators responsive to the feedback circuit;
- the control circuits also being responsive to current
  measurements through the first buck converter circuit and the
- 19 second buck converter circuit to adjust the relative duty cycle
- 20 of the first and second buck converter circuits;

- the first pulse width modulator, the second pulse width
  modulator and the control circuits being in a single integrated
  circuit.
  - a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;

(Twice Amended) A DC to DC converter comprising:

- a plurality of pulse width modulators driven by a common oscillator in an interleaved manner, each pulse width modulator controlling one of the plurality of buck converter circuits, whereby the operation of the buck converter circuits is interleaved;
  - a feedback circuit responsive to a voltage on the common output;
- a voltage control circuit for controlling the plurality of pulse width modulators responsive to the feedback circuit and a commanded output voltage; and
- a current balance control circuit for controlling the pulse
  width modulators responsive to a difference in currents [current]
  in the inductors of the plurality of interleaved buck converter
  circuits to balance the currents [current] in the plurality of
  interleaved buck converter circuits;

51.

1

2

3

12

- the plurality of pulse width modulators and the control 22 23 circuits being in a single integrated circuit.
  - (Twice Amended) A DC to DC converter having a 52. 1 plurality of converter circuits operating into a common load, 2 comprising: 3
    - a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;
- a plurality of pulse width modulators each controlling power 9 switching devices of one of the plurality of buck converter 10 circuits, the operation of the pulse width modulators and the 11 buck converter circuits being interleaved;
- a feedback circuit responsive to a voltage across the common 13 load; 14
- control circuits being responsive to the feedback circuit 15 and a commanded output voltage to control a nominal duty cycle of 16 the plurality of buck converter circuits, the control circuits 17 also being responsive to the difference in currents in the 18 19 plurality of interleaved buck converter circuits to adjust the relative duty cycles [cycle] \ of the plurality of buck converter 20 circuits to balance the currents [current] in the buck converter 21 22 circuits;

- 23 the plurality of pulse width modulators and the control
- 24 circuits being in a single integrated circuit.
  - 1 53. (Twice Amended) A DC to DC converter having first and
  - 2 second converter circuits operating into a common load,
  - 3 comprising:

first and second buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power

- 7 supply terminal and the common load, [terminals,] and the second
- 8 power supply terminal and the common load;
- a first pulse width modulator controlling the first buck
- 10 converter circuit;
- a second pulse width modulator controlling the second buck
- 12 converter circuit;
- a feedback circuit responsive to the voltage across the
- 14 common load;
- control circuits for controlling the first and second pulse
- 16 width modulators responsive to the feedback circuit;
- the control circuits also being responsive to current
- 18 measurements in the first buck converter circuit and the second
- 19 buck converter circuit to adjust the relative duty cycle of the
- 20 first and second buck converter circuits;

- 21 the first pulse width modulator, the second pulse width
- 22 modulator, the feedback circuit and the control circuits being in
- 23 a single integrated circuit.
- 1 54. (Twice Amended) A DC to DC converter having a
- 2 plurality of converter circuits operating into a common load,
- 3 comprising:
- a plurality of buck converter circuits operating into the
- 5 common load, each buck converter circuit having an inductor for
- 6 alternately conducting between the first [and second] power
- 7 supply terminal and the common load, [terminals,] and the second
- 8 power supply terminal and the common load;
- a plurality of pulse width modulators driven by a common
- 10 oscillator in an interleaved manner, each pulse width modulator
- 11 controlling one of the plurality of buck converter circuits,
- 12 whereby the operation of the buck converter circuits is
- 13 interleaved;
- a feedback circuit responsive to a voltage across the common
- 15 load;
- a voltage control circuit for controlling the plurality of
- 17 pulse width modulators responsive to the feedback circuit and a
- 18 commanded output voltage; and
- a current balance control circuit for controlling the pulse
- 20 width modulators to balance the currents [current] in the
- 21 plurality of interleaved buck converter circuits responsive to

- 22 the difference in currents [current] in the plurality of
- 23 interleaved buck converter circuits:
- the plurality of pulse width modulators, the voltage control
- 25 circuit and the current balance control circuit being in a single
- 26 integrated circuit.
  - 55. (Twice Amended) A DC to DC converter having a plurality of converter circuits operating into a common load, comprising:
  - a plurality of buck converter circuits operating into the
  - 5 common load, each buck converter circuit having an inductor for
  - 6 alternately conducting between the first [and second] power
  - 7 supply terminal and the common load, [terminals,] and the second
  - 8 power supply terminal and the common load;
  - a plurality of pulse width modulators each controlling power
- 10 switching devices of one of the plurality of interleaved buck
- 11 converter circuits, the operation of the pulse width modulators
- 12 and the buck converter circuits being interleaved;
- a feedback circuit responsive to a voltage across the common
- 14 load;
- 15 control circuits responsive to the feedback circuit and a
- 16 commanded output voltage to control a nominal duty cycle of the
- 17 plurality of buck converter circuits, the control circuits also
- 18 adjusting [a] relative duty cycles [cycle] of the plurality of
- 19 buck converter circuits to balance the currents [current] in the

- 20 buck converter circuits responsive to the difference in currents
- 21 [current] in the plurality of interleaved buck converter
- 22 circuits;
- the plurality of pulse width modulators and the control
- 24 circuits being in a single integrated circuit.
  - 56. (Twice Amended) A DC to DC converter having a plurality of converter circuits operating into a common load, comprising:
  - a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;
  - a plurality of pulse width modulators each controlling one of the plurality of buck converter circuits, the pulse width modulators being driven by a common oscillator signal so that the operation of the pulse width modulators is interleaved;
- control circuits for adjusting a nominal duty cycle of the
  plurality of interleaved buck converter circuits to control a
  voltage on the common load, and for adjusting [a] relative duty
  cycles [cycle] of the plurality of buck converter circuits to
  balance the currents [current] in the buck converter circuits;
- the plurality of pulse width modulators and the control
- 19 circuits being in a single integrated circuit.

- 57. (Twice Amended) A DC to DC converter having first and second buck converter circuits operating into a common load, comprising:

  first and second buck converter circuits operating into the common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;
  - a first pulse width modulator controlling the first buck converter circuit;
- a second pulse width modulator controlling the second buck converter circuit;
- a feedback circuit responsive to the voltage across the common load;
  - control circuits for controlling the first and second pulse width modulators responsive to the feedback circuit;
- the control circuits also being responsive to current
- 18 measurements in the first buck converter circuit and the second
- 19 buck converter circuit to \adjust the relative duty cycle of the
- 20 first and second pulse width modulators to balance the currents
- 21 <u>in the</u> buck converter circulits;
- the first pulse width modulator, the second pulse width
- 23 modulator and the control circuits being in a single integrated
- 24 circuit.

1 58. (Amended) A DC to DC converter having a plurality of
2 converter circuits for operating into a common load, comprising:
3 a plurality of buck converter circuits operating into the
4 common load, each buck converter circuit having an inductor for
5 alternately conducting between the first [and second] power
5 supply terminal and the common load, [terminals,] and the second

power supply terminal and the common load;

a plurality of pulse width modulators driven by a common oscillator in an interleaved manner, each pulse width modulator controlling one of the plurality of buck converter circuits, whereby the operation of the buck converter circuits is interleaved;

a feedback circuit responsive to a voltage across the common output;

a voltage control circuit controlling the plurality of pulse width modulators responsive to the feedback circuit and a commanded output voltage;

the plurality of pulse width modulators and the control circuits being in a single integrated circuit.

60. (Amended) A DC to DC converter having a plurality of converter circuits operating into a common load, comprising:

a plurality of buck converter circuits operating into the common load, each buck converter circuit having an inductor for

46

8

9

10

11

12

15

16

- 5 alternately conducting between the first [and second] power
- 6 supply terminal and the common load, [terminals,] and the second
- 7 power supply terminal and the common load;
- a plurality of pulse width modulators each controlling one
- 9 of the plurality of buck converter circuits, the operation of the
- 10 pulse width modulators and the buck converter circuits being
- 11 interleaved;
- a feedback circuit responsive to a voltage across the common
- 13 load;
- control circuits responsive to the feedback circuit and a
- 15 commanded output voltage to control a nominal duty cycle of the
- 16 plurality of buck converter circuits;
- the plurality of pulse width modulators and the control
- 18 circuits being in a single integrated circuit.

## 62. (Amended) A Do to DC converter comprising:

first and second buck converter circuits operating into a common load, each buck converter circuit having an inductor for alternately conducting between the first [and second] power supply terminal and the common load, [terminals,] and the second power supply terminal and the common load;

- first and second pulse width modulators driven by a common
- 8 oscillator in an interleaved manner, each pulse width modulator
- 9 controlling a respective one of the first and second buck

- 10 converter circuits, whereby the operation of the buck converter
- 11 circuits is interleaved;
- a feedback circuit responsive to a voltage across the common
- 13 output;
- a voltage control circuit controlling the first and second
- 15 pulse width modulators responsive to the feedback circuit and a
- 16 commanded output voltage;
- the plurality of pulse width modulators and the control
- 18 circuits being in a single integrated circuit.

## 64. (Amended) A DC to DC converter comprising:

first and second buck converter circuits operating into a

- 3 common load, each buck converter circuit having an inductor for
- 4 alternately conducting between the first [and second] power
- 5 supply terminal and the common load, [terminals,] and the second
- 6 power supply terminal and the common load;
  - first and second pulse width modulators each controlling a
- 8 respective one of the buck converter circuits, the operation of
- 9 the pulse width modulators and the buck converter circuits being
- 10 interleaved;
- a feedback circuit responsive to a voltage across the common
- 12 load;
- control circuits responsive to the feedback circuit and a
- 14 commanded output voltage to control a nominal duty cycle of the
- 15 plurality of buck converter dircuits;

- the plurality of pulse width modulators and the control
- 17 circuits being in a single integrated circuit.